

Market Trends & Supply Chain Issues Report: **Specialty and Formulated Cleans** *A TEHCET Critical Materials Report TM*

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RESEARCH METHODOLOGY

TEHCET employs subject matter experts having first-hand experience within the industries which they analyze. Most of TEHCET's analysts have over 25 years of direct and relevant experience in their field. Our analysts survey the commercial and technical staff of IC manufacturers and their suppliers and conduct extensive research of literature and commerce statistics to ascertain the current and future market environment and global supply risks. Combining this data with TEHCET's proprietary, quantitative wafer forecast results in a viable long-term market forecast for a variety of process materials.

Readers Note: This report represents the interpretation and analysis of information generally available to the public or released by responsible agencies or individuals. Data was obtained from sources considered reliable. However, accuracy or completeness is not guaranteed.

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1 Scope

This report focuses on the market and technical trends related to specialty cleaning chemicals used for silicon semiconductor device manufacturing, including post etch residue removers (PERRs) and post CMP (pCMP) cleaning chemicals. It specifically addresses: what is changing in the market? Why is it changing? and What are the growth opportunities for suppliers interested in this market?

Details on post CMP cleaning chemistries and challenges surrounding cleaning mechanisms are provided. Similar details on Post etch residue remover chemistries are not included in this year's report due to the well-hidden trade secrets behind these blends. For more information, please see TECHCET's 2017 report on Electronic Wet Chemicals or contact us at info@techcet.com.